



09/28/00

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>car</u> Q.A. <u>LT</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/672650	CONT/PRIOR	CLASS 711 714	SUBCLASS 711	ART UNIT 2187 2187	EXAMINER Chase
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Memory tester has memory sets configurable for use as error catch
RAM, tag RAM's, buffer memories and stimulus log RAM

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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